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Apache Design Solutions, Inc. is an Oregon Foreign Business Corporation filed on March 27, 2009. The company's filing status is listed as

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Inactive and its File Number is 577120-94. The Registered Agent on file for this company is Andrew Yang and is located at 5677 Ne Vino Ct, Hillsboro, OR 97124. The company's principal address is 2645 Zanker Rd 2nd Fl, San Jose, CA 95134 and its mailing ...

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Registered Agent on file for this company is Corporation Service Company and is located at 84 State St., Boston, MA 02109.

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optimization solutions that enable the design of power-efficient, high-performance, noise-immune ICs and electronic systems, today announced that it has filed a registration statement on Form S-1 with the Securities ...

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Page 25/57

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methods, design by optimization, statistical design optimization, and physical design automation. In the second half of the text, they turn their attention to RF CAD, high performance simulation, formal verification, RTK behavioral synthesis, system-level design, an Internet-based

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micro-electronic design automation framework, performance modeling, and embedded computing systems design.

Fabless (no fabrication) IC (integrated circuit) techniques are growing rapidly and promise to become the standard

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method of IC manufacturing in the near future, this book will provide readers with what will soon be required knowledge of the subject. Other books on IC fabrication deal with the strictly physical process aspects of the topic and assume all factors in IC fabrication are under the control of the

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IC designing company. By contrast, this title recognizing that fabless IC design is often as much about managing business relationships as it is about physical processes. “Fabless ICs are those designed and marketed by one company but actually manufactured by another. *Written by

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board members of the Fabless Semiconductor Association, an industry consortium that include Xilinx, Intersil, Micro Linear, and many other members *Appropriate for a wide range of integrated circuit (IC) designers and users who need to understand the fabless process and its

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advantages/limitations *Discusses important topics such as negotiating with outside fabrication companies, choosing the right electronic design tools, protection of intellectual property and business plans, and maintaining quality control

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Annotation Fifty-one papers and 21 posters from the March 2002 symposium report current research in deep submicron integrated circuit design and development. The sessions address interconnect extraction and modeling, design for process variations, metrics, power and

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noise management, verification, signal integrity, and low power design techniques. Some of the topics are transition aware global signaling (TAGS), the interoperability of EDA tools for sequential logic synthesis, statistical methods for the determination of process corners,

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power supply noise suppression via clock skew scheduling, and the relation between SAT and BDDs for equivalence checking. No subject index. Annotation copyrighted by Book News Inc., Portland, OR.

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Java developers who are interested in building web services using Apache Axis2. The book presumes that you are familiar with web standards like SOAP, WSDL and XML parsing.

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4